No.	LD – 19Z53A
DATE	DEC.7.2007
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Rev.

TECHNICAL LITERATURE

FOR

TFT - LCD module

These parts have corresponded with the RoHS directive.

MODEL No. LQ10D36A

The technical literature is subject to change without notice. So, please contact SHARP or its representative before designing your product based on this literature.

ENGINEERING DEPARTMENT MOBILE LIQUID CRYSTAL DISPLAY DIVISION MOBILE LIQUID CRYSTAL DISPLAY GROUP SHARP CORPORATION

RECORDS OF REVISION

SPEC No. DATE FAGE SUMMARY NOTE LD-19Z53A DEC.7.2007 - - 1 st Issue LD-19Z53A ISC.7.2007 - - - ISC.7.2007 ISC.7.2007 - - - ISC.7.2007 ISC.7.2007 - - - - ISC.7.2007 ISC.7.2007 ISC.7.2007 - - - - ISC.7.2007 ISC.7.2007 ISC.7.2007 ISC.7.2007 - - - - <th>LQ10D36A</th> <th></th> <th></th> <th></th> <th></th>	LQ10D36A				
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1. Application

This technical literature applies to color TFT-LCD module, LQ10D36A

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2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, control circuit and power supply circuit and a backlight unit. Graphics and texts can be displayed on a $640 \times 3 \times 480$ dots panel with 262,144 colors by supplying 18 bit data signal (6bit/color), four timing signals,+3.3V/+5.0V DC supply voltage for TFT-LCD panel driving and supply voltage for backlight.

The TFT-LCD panel used for this module is a low-reflection and higher-color-saturation type. Therefore, this module is also suitable for the multimedia use. Optimum viewing direction is 6 o'clock. Backlight-driving DC/AC inverter is not built in this module.

Parameter	Specifications	Unit
Display size	26 (10.4") Diagonal	cm
Active area	211.2(H) × 158.4(V)	mm
Pixel format	640(H) × 480(V)	pixel
	$(1 \text{ pixel} = \mathbf{R} + \mathbf{G} + \mathbf{B} \text{ dots})$	
Pixel pitch	0.330(H) × 0.330(V)	mm
Pixel configuration	R,G,B vertical stripe	
Display mode	Normally white	
Unit outline dimensions *1	$246.5(W) \times 179.4(H) \times 11.0max(D)$	mm
Mass	520(max)	g
Surface treatment	Anti-glare and hard-coating 3H	

3. Mechanical Specifications

*1.Note: excluding backlight cables.

Outline dimensions is shown in Fig.1(TBD)

4. Input Terminals

4-1. TFT-LCD panel driving

CN1Corresponding connector : DF9-31S-1V(32) (" 1 1 3 DF9A-31S-1V(22) (" 2 1 3 DF9B-31S-1V(32) ("CN1 pin arrangement from module surfaceDF9M-31S-1V(32) (" $(Transparent view)$ () Do not use it besides corresponding conector. $\overline{Pin No.}$ SymbolFunctionRemark 2 CKClock signal for sampling each data signal(3 HsyncHorizontal synchronous signal(4 VsyncVertical synchronous signal(4 VsyncVertical signal(LSB)7 7 RIR E Ddata signal 8 R2R E Ddata signal 9 R3R E Ddata signal 11 R5R E Ddata signal 12 GND11 14 G1G R E E N 13 G0G R E E N 14 G1G R E E N 15 G2G R E E N 14 G1G R E E N 17 G4G R E E N 18 G5G R E E N 19 GND 19 GND 10 R E D 14 G5 14 G1 15 G2 14 G1 15 G2 14 G1 E E N 15 G2 16 G3 17 G4 18 G5 19 GND 19 10 <tr< th=""><th>., Ltd.)</th></tr<>	., Ltd.)
2 1 1 3d DF9B-31S-1V(32) (" CNI pin arrangement from module surface (Transparent view) DF9M-31S-1V(32) (" " 1 GND Function Remark 2 CK Clock signal for sampling each data signal Remark 3 Hsync Horizontal synchronous signal (Note1) 4 Vsync Vertical synchronous signal (Note1) 5 GND 5 GND 6 R0 R E D data signal(LSB) 7 R1 R E D data signal 9 R3 R E D data signal 10 R4 R E D data signal 11 R5 R E D data signal 11 R5 R E D data signal 12 GND)
CNI pin arrangement from module surface (Transparent view) DF9M-31S-1V(32) ("" Pin No. Symbol Function Remark 1 GND GND Remark 2 CK Clock signal for sampling each data signal [Note1] 3 Hsync Horizontal synchronous signal [Note1] 4 Vsync Vertical synchronous signal [Note1] 5 GND [Note1] [Note1] 6 R0 R E D data signal 8 R2 R E D data signal 9 R3 R E D data signal 11 R5 R E D data signal 9 R3 R E D data signal 11 R5 R E D data signal 12 GND [Intervention] [Intervention] 13 G0 G R E E N data signal 14 G1 G R E E N data signal 15 G2 G R E E N data signal 16 G3 G R E E N data signal 17 G4 <td>)</td>)
(Transparent view)() Do not use it besides corresponding conector.Pin No.SymbolFunctionRemark1GND)
Pin No.SymbolFunctionRemark1GND)
Pin No.SymbolFunctionRemark1GND	
2CKClock signal for sampling each data signal3HsyncHorizontal synchronous signal【Note1】4VsyncVertical synchronous signal【Note1】5GND6R0R E Ddata signal(LSB)7R1R E Ddata signal8R2R E Ddata signal9R3R E Ddata signal10R4R E Ddata signal11R5R E Ddata signal(LSB)12GND13G0G R E E Ndata signal15G2G R E E Ndata signal16G3G R E E Ndata signal17G4G R E E Ndata signal18G5G R E E Ndata signal19GND	
3 Hsync Horizontal synchronous signal [Note1] 4 Vsync Vertical synchronous signal [Note1] 5 GND [Note1] 6 R0 R E D data signal(LSB) 7 R1 R E D data signal 8 R2 R E D data signal 9 R3 R E D data signal 10 R4 R E D data signal 11 R5 R E D data signal(MSB) 12 GND [Note1] 13 G0 G R E E N data signal 14 G1 G R E E N data signal 15 G2 G R E E N data signal 17 G4 G R E E N data signal 18 G5 G R E E N data signal 18 G5 G R E E N data signal 19 GND [Note1] [Note1]	
4VsyncVertical synchronous signal[Note1]5GND	
5 GND GND 6 R0 R E D data signal(LSB) 7 R1 R E D data signal 8 R2 R E D data signal 9 R3 R E D data signal 10 R4 R E D data signal 11 R5 R E D data signal 12 GND Image: GND Image: GND 13 G0 G R E E N data signal(LSB) 14 G1 G R E E N data signal 15 G2 G R E E N data signal 16 G3 G R E E N data signal 17 G4 G R E E N data signal 18 G5 G R E E N data signal(MSB) 19 GND Image: GND Image: GND	
6R0R E Ddata signal(LSB)7R1R E Ddata signal8R2R E Ddata signal9R3R E Ddata signal10R4R E Ddata signal11R5R E Ddata signal(MSB)12GNDImage: GND13G0G R E E Ndata signal(LSB)14G1G R E E Ndata signal15G2G R E E Ndata signal16G3G R E E Ndata signal17G4G R E E Ndata signal18G5G R E E Ndata signal(MSB)19GNDImage: GNDImage: GND	
7R1R E Ddata signal8R2R E Ddata signal9R3R E Ddata signal10R4R E Ddata signal11R5R E Ddata signal(MSB)12GNDImage: GND13G0G R E E Ndata signal(LSB)14G1G R E E Ndata signal15G2G R E E Ndata signal16G3G R E E Ndata signal17G4G R E E Ndata signal18G5G R E E Ndata signal(MSB)19GNDImage: GND	
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9R3R E Ddata signal10R4R E Ddata signal11R5R E Ddata signal(MSB)12GNDIII13G0G R E E N14G1G R E E N15G2G R E E N16G3G R E E N17G4G R E E N18G5G R E E N19GND	
10R4R E Ddata signal11R5R E Ddata signal(MSB)12GND13G0G R E E Ndata signal(LSB)14G1G R E E Ndata signal15G2G R E E Ndata signal16G3G R E E Ndata signal17G4G R E E Ndata signal18G5G R E E Ndata signal(MSB)19GND	
11R5R E Ddata signal(MSB)12GND13G0G R E E Ndata signal(LSB)14G1G R E E Ndata signal15G2G R E E Ndata signal16G3G R E E Ndata signal17G4G R E E Ndata signal18G5G R E E Ndata signal(MSB)19GND	
12GND13G0G R E E Ndata signal(LSB)14G1G R E E Ndata signal15G2G R E E Ndata signal16G3G R E E Ndata signal17G4G R E E Ndata signal18G5G R E E Ndata signal(MSB)19GND	
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14G1G R E E Ndata signal15G2G R E E Ndata signal16G3G R E E Ndata signal17G4G R E E Ndata signal18G5G R E E Ndata signal(MSB)19GND	
15G2G R E E Ndata signal16G3G R E E Ndata signal17G4G R E E Ndata signal18G5G R E E Ndata signal(MSB)19GND	
16 G3 G R E E N data signal 17 G4 G R E E N data signal 18 G5 G R E E N data signal(MSB) 19 GND	
17 G4 G R E E N data signal 18 G5 G R E E N data signal(MSB) 19 GND	
18 G5 G R E E N data signal(MSB) 19 GND	
19 GND	
20 B0 B L U E data signal(LSB)	
21 B1 BLUE data signal	
22 B2 B L U E data signal	
23 B3 B L U E data signal	
24 B4 B L U E data signal	
25 B5 B L U E data signal(MSB)	
26 GND	
27ENABSignal to settle the horizontal display position【Note2】	
28 Vcc + 3.3/5.0V power supply	
29 Vcc + 3.3/5.0V power supply	
30 R/L Horizontal display mode select signal [Note3]	
31 U/D Vertical display mode select signal 【Note4】	

The shielding case is connected with GND.

[Note1] 480 line, 400 line or 350 line mode

is selected by the polarity combination

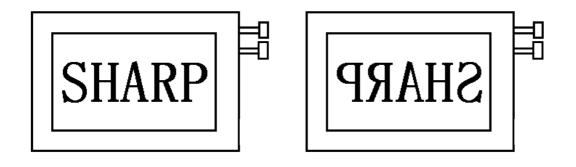
of the both synchronous signals.

Mode	480 lines	400 lines	350 lines
Hsync	Negative	Negative	Positive
Vsync	Negative	Positive	Negative

[Note2] The horizontal display start timing is settled in

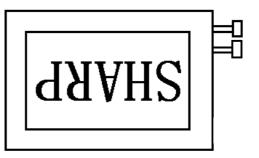
accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined as described in 7-2. Do not keep ENAB "High" during operation.

[Note3] [Note4]



R / L = H i g h、U / D = L o w R / L = L o w、U / D = L o w





R/L=High, U/D=High

R∕L=Low、U∕D=High

4-2. Backlight driving

Used connector : BHR-02(8.0)VS-1N (JST)

					(,
С	N2, CN3		Corresponding connector : SM0	2(8.0)B-BHS	(JST)
	Pin no.	Symbol	Function	Cable color	
	1	$V_{\rm HIGH}$	Power supply for lamp	Pink	
			(High voltage side)		
	2	NC	This is electrically opened.		
	3	$V_{\rm LOW}$	Power supply for lamp	White	
			(Low voltage side)		

5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
Input voltage	VI	Ta=25	$-0.3 \sim Vcc + 0.3$	V	[Note1]
+5V supply voltage	Vcc	Ta=25	0 ~ + 6	V	
Storage temperature	Tstg	-	- 30 ~ + 70		[Note2]
Operating temperature (Ambient)	Тора	-	- 10 ~ +65		

[Note1] CK, R0 ~ R5, G0 ~ G5, B0 ~ B5, Hsync, Vsync, ENAB, R/L, U/L

[Note2] Humidity: 95%RH Max. at Ta 40 .

Maximum wet-bulb temperature at 39 $\,$ or less at Ta>40 $\,$.

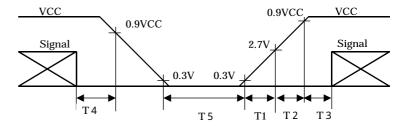
No condensation.

6. Electrical Characteristics

5-1.TFT-LCDpaneldriving						Ta = 25	5
	Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Power	Supply voltage	Vcc	+3.0	+3.3 +5.0	+5.5	V	[Note1]
Supply	Current dissipation	Icc	-	(200)	(300)	m A	Vcc=3.3V [Note2]
		Icc	-	(130)	(200)	m A	Vcc=5.0V [Note2]
Permi	issive input ripple voltage	V _{RF}	-	-	100	mVp-p	
Input	voltage (Low)	V _{IL}	-	-	0.8	V	
Input	voltage (High)	V _{IH}	2.1	-	-	V	[Note3]
Inp	out current (low)	I _{OL1}	-	-	1.0	μA	V _I =0V 【Note4】
_		I _{OL2}			10	μA	$V_{I}=0V$ [Note5]
		I _{OL3}	-	-	800	μA	V _I =0V [Note6]
Input current (High)		I _{OH1}	-	-	1.0	μA	V _I =Vcc [Note7]
_		I _{OH2}			300	μA	V _I =Vcc [Note8]
		I _{OH3}	-	-	800	μA	V _I =Vcc [Note9]

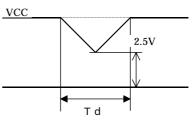
[NOTE 1] Vcc-turn-on conditions

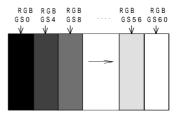
0 < T 1	15 m s
0 < T 2	1 0 m s
0 < T 3	100ms
0 < T 4	1 s
T 5 >	200ms



Vcc-dip conditions

- 1) 2.5V Vcc td 10ms 2) Vcc<2.5V Vcc-dip condition should also follow the Vcc-turn-on conditions.
- [Note2] Typical current situation : 16-gray-bar pattern. 480 line mode/Vcc=+3.3V/ + 5.0V
- [Note3] CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L, U/D
- [Note4] CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync,
- [Note5] U/D, ENAB
- [Note6] R/L
- [Note7] CK, R0~R5, G0~G5, B0~B5, Hsnc, Vsync, R/L
- [Note8] ENAB
- [Note9] U/D





6-2. Backlight driving

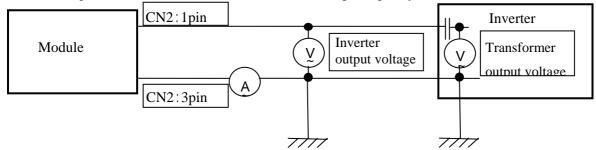
The backlight system is an edge-lighting type with single CCFT (Cold Cathode Fluorescent Tube). The characteristics of single lamp are shown in the following table.

(It is usually required to measure under the following condition.

•••••••••••••••••••••••••••••••••••••••		,				
Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Lamp current	Il	2.0	6.0	6.5	mArms	[Note1]
Lamp power consumption	PL	-	(3.0)	-	W	[Note2]
Lamp frequency	Fl	20	35	60	KHz	[Note3]
Kick-off voltage	Vs	-	-	(950)	Vrms	Ta= 25 【Note4】
		-	-	(1400)	Vrms	Ta = 0 [Note4]
		-	-	(1500)	Vrms	Ta = -10 [Note4]

condition:IL=6.0mA,Ta= 25 ± 2 ,FL=60kHz.)

[Note1] Lamp current is measured with current meter for high frequency as shown below.



[Note2] Referential data per one CCFT by calculation. (IL \times VL) The data do not include loss at inverter. (IL=6.0mArms)

- [Note3] Lamp frequency may produce interference with horizontal synchronous frequency, and this may cause beat on the display. Therefore lamp frequency shall be detached as much as possible from the horizontal synchronous frequency and from the harmonics of horizontal synchronous to avoid interference.
- [Note4] The open output voltage of the inverter shall be maintained for more than 1s; otherwise

the lamp may not be turned on.

[Note5] lamp is an article of consumption. Lamp life time is 50,000 hours (reference value) on condition of the following, but this value is not guaranteed.

When lamp (the long side of LCD module) is placed horizontally (landscape position), lamp life time is defined that it applied either or under this condition.

(Continuous turning on at Ta=25 °C, IL=6.0mA rms.)

Brightness becomes 50% of the original value under standard condition.

Kick-off voltage at Ta=-10 °C exceeds maximum value.

(Lamp lifetime may vary if lamp is in portrait position due to the change of mercury density inside the lamp.)

In case of operating under lower temp environment, the lamp exhaustion is accelerated and the brightness becomes lower.

(Continuous operating under lower temp condition for around 1 month may reduce the brightness to half of the original brightness.)

In case of such usage under lower temp environment, periodical lamp exchange is recommended.

[Note6] The performance of the backlight, for example life time or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. When you design or order the inverter, please make sure that a poor lighting caused by the mismatch of the backlight and the inverter (miss-lighting,flicker, etc.) never occur. when you confirm it, the module should be operated in the same condition as it is installed in your instrument.

Be sure to use a back light power supply with the safety protection circuit such as the detection circuit for the excess voltage, excess current and or electric discharge waveform.

Be sure to use the detect circuit by which one side of the CCFT lamps can be controlled independently. Otherwise, when one side of the CCFT is open, the excess current may possibly be applied to the other side of the lamp.

[Note7] Under the environment of 10lx or less, miss-lighting delay may occur.

7. Timing Characteristics of input signals

Timing diagrams of input signal are shown in Fig.2 - ~

7-1. I HIIII CHALACTERISTICS	7-1.	Timing	characteristics
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Parar	Symbol	Mode	Min.	Тур.	Max.	Unit	Remark	
Clock	Frequency	1/Tc	all	-	25.18	28.33	MHz	
	High time	Tch	"	5	-	-	ns	
	Low time	Tcl	"	10	-	-	ns	
Data	Setup time	Tds	"	5	-	-	ns	
	Hold time	Tdh	"	10	-	-	ns	
Horizontal	Cycle	TH	"	30.00	31.78	-	μs	
sync. signal			"	750	800	900	clock	
	Pulse width	ТНр	"	2	96	200	clock	
Vertical	Cycle	TV	480	515	525	560	line	
sync. signal			400	446	449	480	line	
			350	447	449	510	line	
	Pulsewidth	TVp	all	1	-	34	line	
Horizontal di	splay period	THd	"	640	640	640	clock	
Hsync-Clock	THc	"	10	-	Tc-10	ns		
Phase differen	nce							
Hsync-Vsync		TVh	"	0	-	TH-THp	clock	
Phase differen	nce							

Note) In case of lower frequency, the deterioration of display quality, flicker etc., may be occurred.

7-2. Horizontal display position

The horizontal display position is determined by the rising edge of ENAB signal.

Paran	neter	symbol	Min.	Тур.	Max.	Unit	Remark
Enable signal	Setup time	Tes	5	-	Tc-10	ns	
	Pulse width	Тер	2	640	640	clock	
Hsync-Enable	Hsync-Enable signal			-	TH-664	clock	
phase differen	ce						

Note) When ENAB is fixed "Low", the display starts from the data of C104(clock) as shown in Fig.2- ~ When the phase difference is below 104 clocks, keep the "High" level of ENAB is signal longer than 104-The clocks. If it will not be kept, the display starts from the data of C104(clock).

7-3. Vertical display position

The vertical display position is automatically centered in the active area at each mode of VGA 480, 400, and 350-line mode, when each mode is selected depending on the polarity of the synchronous signals described in 4-1(Note1).

In 400- and 350-line mode, when vertical synchronous signal TV is timing other than

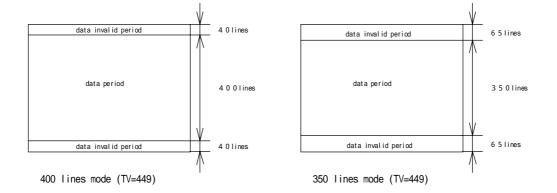
the above-mentioned typical value, a screen display position may shift.

In 400- and 350-line mode, the data in the vertical data invalid period is also displayed,

so, inputting all data "0" is recommended during vertical data invalid period.

Mode	V-data start(TVs)	V-data	V-display start(TVn)	V-display period	Unit	Remark
		period(TVd)				
480	34	480	34	480	line	
400	34	400	443-TV	480	line	
350	61	350	445-TV	480	line	

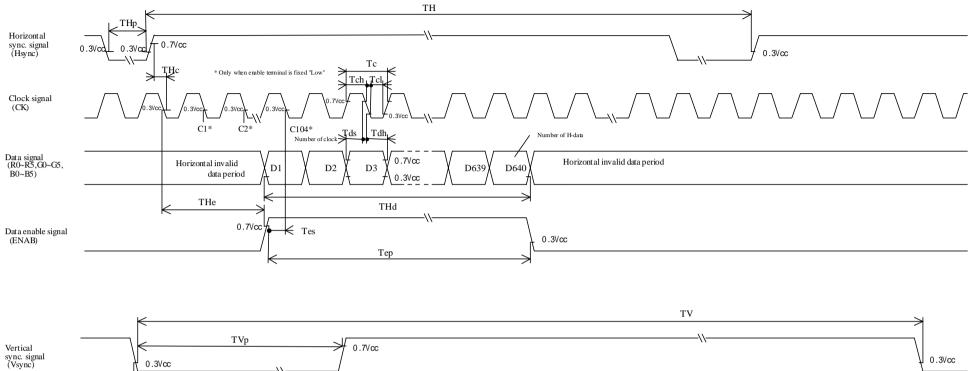
ENAB signal has no relation to the vertical display position.

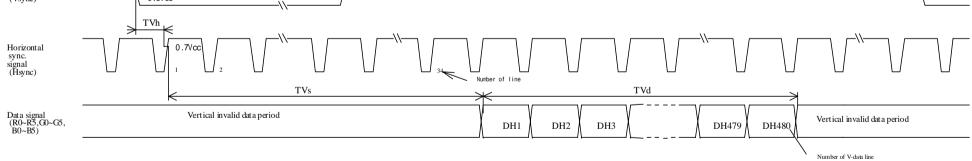


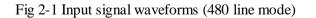
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7-4. Input Data Signals and Display Position on the screen

 Dis	splay pos	ition of	input data (480 lin (H,V)	es mode)	UΡ
D1,DH1	D2,DH1	D3,DH1		D640,DH1	
D1,DH2	D2, DH2				
D1,DH3					
			R G B		
D1,DH480				D640,DH480	







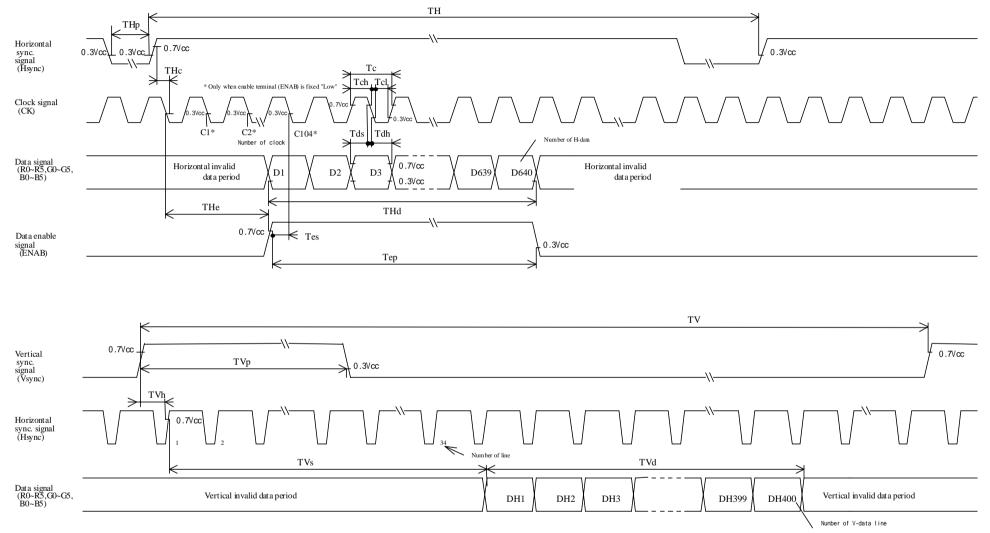


Fig.2-2 Input signal waveforms (400 line mode)



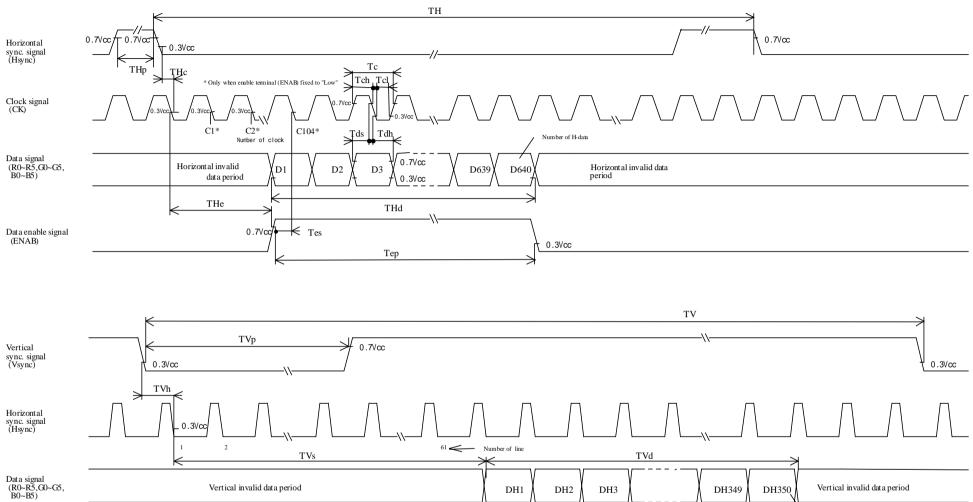


Fig.2-3 Input signal waveforms (350 line mode)

Number of V-data line

8. Input Signals, Basic Display Colors and Gray Scale of Each Color

	Colors &		Data signal																	
	Gray scale	Gray	R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	B0	B1	B2	B3	B4	B5
		Scale																		
	Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	-	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Ba	Green	-	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic Color	Cyan	-	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
òlor	Red	-	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	-	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	-	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gra	仓	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
S &	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ale	仓	\checkmark			``	V											``	\boldsymbol{k}		
Gray Scale of Red	Û	\checkmark			``	V				\checkmark					\checkmark					
a a	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Û	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Graj	仓	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
/ Sc	Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
Gray Scale of	仓	\checkmark			``	V											``	L I		
f Green	Û	\checkmark			``	V											``	arepsilon		
B B	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
	Û	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray	仓	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
28	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
Gray Scale of Blue	仓	\checkmark			``	r											``	r		
Blu	Û	\checkmark			``	r											``	r		
Ō	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
	Û	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

0 :Low level voltage, 1 : High level voltage

Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.

9. Optical Characteristics

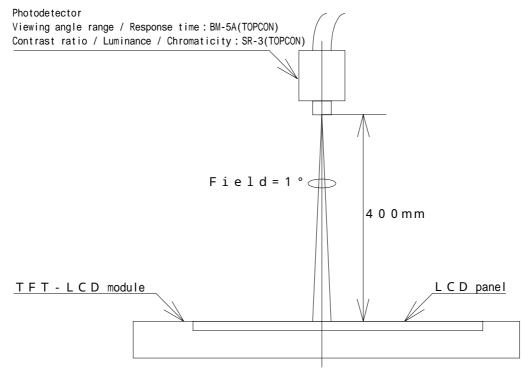
```
Ta=25 , Vcc=+5V
```

Optical Charact	eniberes					1,	1-25, 1	
Parar	Parameter		Condition	Min	Тур	Max	Unit	Remark
Viewing	ing Horizontal 21, 22		C R > 1 0	60	70	-	Deg.	[Note1,4]
Angle	Vertical	11		35	40	-	Deg.	
Range		12		55	70	-	Deg.	
Contrast rat	tio	C R	= 0 °	150	-	-	-	[Note2,4]
			Optimum	-	300	-	-	
			Viewing Angle					
Response	Rise	r	= 0 °	-	10	-	ms	[Note3,4]
Time	Decay	d		-	25	-	ms	
Chromat	icity of	x		-	0.313	-		[Note4]
Wh	nite	У		-	0.329	-		I _L =6.0mArms
Luminance	of white	ΥL		(160)	(200)	-	cd/m ²	f=60kHz
White Unif	omity	w		-	-	1.45	-	[Note5]
Viewing	Horizontal	21, 22	50% of	-	(45)	-	Deg.	[Note1]
Angle			the					
range as a	Vertical	11	maximum		(35)	_	Deg	-
Brightness	ventical	11	brightness		(35)		Deg.	
Definition		١٢		-	(30)	-	Deg.	

The measurement shall be executed 30 minutes after lighting at rating. (condition:IL=6.0mArms, FL=60KHz)

The optical characteristics shall be measured in a dark room or equivalent state

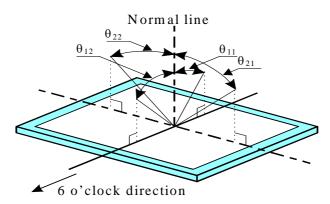
with the method shown in Fig.3 below.



Center of the screen

Fig.3 Optical characteristics measurement method

[Note1] Definitions of viewing angle range:



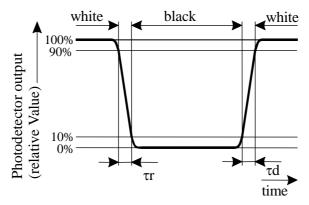
[Note2] Definition of contrast ratio:

The contrast ratio is defined as the following.

Luminance (brightness) with all pixels white Luminance (brightness) with all pixels black

[Note3] Definition of response time:

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



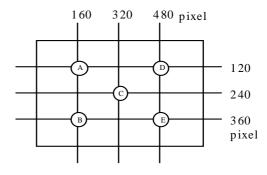
[Note4] This shall be measured at center of the screen.

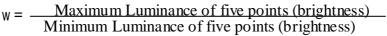
[Note5] Definition of white uniformity:

White uniformity is defined as the

following with five measurements

 $(A \sim E).$





10. Display Quality

The display quality of the color TFT-LCD module shall be in compliance with the Incoming Inspection Standard.

11 . Handling Precautions

- a) Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.
- c) Since the front polarizer is easily damaged, pay attention not to scratch it.
- d) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- e) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- f) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
- g) Since CMOS LSI is used in this module, take care of static electricity and injure the human earth when handling. Observe all other precautionary requirements in handling electric components.
- h) Since there is a circuit board in the module back, stress is not added at the time of a design assembly. Please make it like. If stress is added, there is a possibility that circuit parts may be damaged.
- i) Protection film is attached to the module surface to prevent it from being scratched .Peel the film off slowly, just before the use, with strict attention to electrostatic charges.Blow off 'dust' on the polarizer by using an ionized nitrogen.
- j) The polarizer surface on the panel is treated with Anti-Glare for low reflection. In case of attaching protective board over the LCD, be careful about the optical interface fringe etc. which degrades display quality.
- k) Do not expose the LCD panel to direct sunlight. Lightproof shade etc. should be attached when LCD panel is used under such environment. If a light strong against a LCD panel is irradiated, it may lead to degradation of the panel characteristic and display grace may get worse.
- 1) Connect GND to 4 place of mounting holes to stabilize against EMI and external noise.
- m) There are high voltage portions on the backlight and very dangerous. Careless touch may lead to electrical shock. When exchange lamps or service, turn off the power without tail.
- n) When handling LCD modules and assembling them into cabinets, please be noted that long-term storage in the environment of oxidization or deoxidization gas, and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion, discoloration, degradation of display grace, and abnormalities of operation.
- o) Cold cathode fluorescent lamp in LCD panel contains a small amount of mercury, please follow local ordinances or regulations for disposal.
- p) Be careful of a back light lead not to pull by force at the time of the wiring to an inverter, or line processing.
- q) When install LCD modules in the cabinet, recommended torque value is " 0.294 ± 0.02 N· m $(3.0 \pm 0.2$ kgf· cm)".

Be sure to confirm it in the same condition as it is installed in your instrument.

- r) Liquid crystal contained in the panel may leak if the LCD is broken. Rinse it as soon as possible if it gets inside your eye or mouth by mistake.
- s) Notice:Never dismantle the module , because it will cause failure.
- t) Be careful when using it for long time with fixed pattern display as it may cause afterimage.
- u) Adjusting volume have been set optimally before shipment, so do not change any adjusted value.
- If adjusted value is changed, the specification may not be satisfied.
- v) If a minute particle enters in the module and adheres to an optical material, it may cause display nonuniformity issue, etc. Therefore, fine-pitch filters have to be installed to cooling and inhalation hole if you intend to install a fan.
- w) The lamp used for this product is very sensitive to the temperature. Luminance decreases rapidly when it is used for a long time or repeatedly under the environment of the low temperature or the module is being cooled. Please avoid the continuous or repeatedly use of it such an environment.

It may decrease up to 50% of the initial luminance in about one month under the low temperature environment. Please consult our company when it is used under the environment like above mentioned.

12.Packing form

Product countries / Areas	JAPAN, TAIWAN, CHINA
Piling number of cartons	(TBD)
Packing quantity in one carton	(TBD)
Carton size [mm]	(TBD)
Total mass of one carton filled with full modules	(TBD)
Packing form is shown	Fig.4(TBD)

13 . Reliability test items

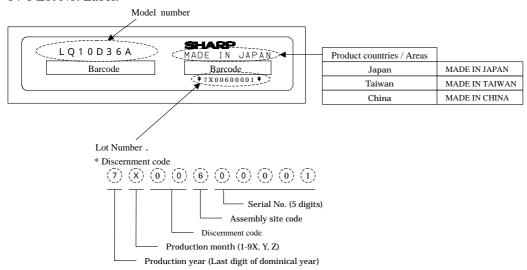
No.	Test item	Conditions	Remark
1	High temperature storage test	Ta=70 240h	Panel surface
2	Low temperature storage test	Ta= -30 240h	
3	High temperature	Ta=40 ; 95%RH 240h	
	& high humidity operation test	(No condensation)	
4	High temperature operation test	Ta=65 240h	Panel surface
5	Low temperature operation test	Ta= -10 240h	
6	Vibration test	Frequency: 10 ~ 57Hz/Vibration width (one side):0.075mm	
	(non- operating)	: 58 ~ 500Hz/Gravity:9.8m/s ²	
		Sweep time : 11 minutes	
		Test period : 3 hours	
		(1 hour for each direction of X,Y,Z)	
7	Shock test	Max. gravity : 490m/s^2	
	(non- operating)	Pulse width : 11ms, half sine wave	
		Direction : $\pm X, \pm Y, \pm Z$ once for each direction.	
8	ESD test	Contact discharge (150pF 330)	
		non-operating = ± 10 kV, operating = ± 8 kV	
		Atmospheric discharge (150pF 330)	
		non-operating = ± 20 kV, operating = ± 15 kV	
9	EMI	Measurement in 10m site	VCCI
		Display position on the screen = "H"(full-screen)	(Class B)
		GND to 4 place = un-connect, $Vcc / Vsignal = typ$.	

[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function. (normal operation state : Temperature: $15 \sim 35$, Humidity: $45 \sim 75\%$, Atmospheric pressure: $86 \sim 106$ kpa)

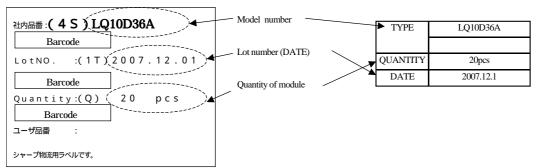
14.0thers

14-1 Lot No. Label:



14-2 Packing box Label: Quantity of module : Japan or China

Quantity of module : Taiwan



The following figure is written to the container box RoHS Compliance corresponded.



R.C. (RoHS Compliance) means these parts have corresponded with the RoHS directive.

14-3 If any problem occurs in relation to the description of this technical literature, it shall be resolved through discussion with spirit of cooperation.

